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Korpinen et al.(10) **Pub. No.: US 2014/0314082 A1**(43) **Pub. Date: Oct. 23, 2014**(54) **METHOD AND AN APPARATUS FOR A GATEWAY****Publication Classification**(51) **Int. Cl.**
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USPC **370/392**(75) Inventors: **Pekka Korpinen**, Espoo (FI); **Juha Rakkola**, Espoo (FI); **Michel Gillet**, Helsinki (FI)(73) Assignee: **Nokia Corporation**, Espoo (FI)(21) Appl. No.: **13/984,143**(22) PCT Filed: **Feb. 25, 2011**(86) PCT No.: **PCT/IB11/50815**

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An apparatus comprises at least one processor and at least one memory including computer code. The at least one memory and the computer code are configured to with the at least one processor cause the apparatus to at least perform receiving a first message from a first port, the first message for transmission in a communications network. The first message comprises at least one destination address. The apparatus is also configured to perform determining whether the first message is deliverable and generating a second message comprising the at least one destination address for each undeliverable address; and transmitting the second message.

